

Amendments to the Claims:

Please amend claims 38 and 40, as noted in the listing below, and also add new claims 45-51 as indicated in the listing. This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

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1. (Previously cancelled).
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38. (Currently Amended) A method comprising:

receiving a command to enter a fast program mode to program a first piece of data at a first address;

- entering into said fast program mode;
- programming said first piece of data at said first address in response to a pulse of a write signal;
- checking whether termination of said fast program mode is indicated or if a second piece of data is to be written, wherein said checking further comprises detecting if an incoming address is different from said first address;
- exiting said fast program mode if said termination of said fast program mode is indicated, else incrementing said first address to a second address; and
- programming said second piece of data at said second address in response to another pulse of said write signal.

39. (Previously Added) The method of claim 38 further comprising receiving a confirmation command prior to entering said program mode.

40. (Currently amended) The method of claim 39 further comprising automatically issuing a status value to indicate a status for said programming of said first piece of data.

41. (Previously Added) The method of claim 40 wherein indication said termination comprises:

receiving an address unequal to said first address and a predefined data packet.

42. (Previously Added) The method of claim 41 wherein said predefined data packet is comprised of all 1's.

43. (Previously Added) The method of claim 41 wherein said predefined data packet is comprised of all 0's.

44. (New) The method of claim 38, wherein said first piece of data is a packet.

45. (New) The method of claim 38, wherein programming said first piece of data further comprises:

erasing the current contents of the first data address.

46. (New) The method of claim 38, wherein programming said second piece of data further comprises:

erasing the current contents of the second data address.

47. (New) A method, comprising:

entering a fast program mode in response to receipt of an initial program mode command and a confirmation program mode command;

writing to a flash memory a plurality of code packets during the fast program mode, the plurality being any of a variable number of packets;

wherein said writing of each said code packet is performed responsive to an associated toggle of a write enable signal; and

exiting the fast program mode in response to a predetermined condition.

48. (New) The method of claim 47, further comprising:

receiving a first address corresponding to a first of the code packets.

49. (New) The method of claim 48, wherein said predetermined condition further comprises:

receiving a second address that is not equal to said first address.

50 (New) The method of claim 47, wherein said predetermined condition further comprises:

receiving a predetermined data value.

51. (New) The method of claim 47, wherein:

said writing is performed in the absence of receipt of any additional program mode commands.